

Description

RETICLE, RETICLE INSPECTION METHOD AND RETICLE INSPECTION APPARATUS

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application is based upon and claims the benefit of priority from the prior Japanese Patent Application No. 2003-394382, filed on November 25, 2003, the entire contents of which are incorporated herein by reference.

BACKGROUND OF THE INVENTION

FIELD OF THE INVENTION

[0002] The present invention relates to a reticle, a reticle inspection method, and a reticle inspection apparatus, and in particular to those preferably used for defect inspection of patterns formed on a reticle.

DESCRIPTION OF THE RELATED ART

[0003] In fabrication process of semiconductor devices such as LSI, patterns (device patterns) are transferred (light exposure) on a semiconductor wafer using a reticle on which

the patterns to be transferred are formed. The reticle after being fabricated is inspected for presence or absence of any defects, where detected defects are corrected.

[0004] Figs. 7A and 7B are flow charts showing conventional procedures for the reticle inspection method.

[0005] Fig. 7A is a flow chart showing process procedures of a preliminary evaluation for determining defect specifications of the reticle for discriminating defect from non-defect.

[0006] First, an evaluation reticle on which a plurality of representative programmed defects (pattern having probable defects) are arranged is fabricated (step S71).

[0007] Next, the programmed defects are transferred onto a semiconductor wafer by light exposure of the evaluation reticle using an actual light exposure apparatus (step S72), and transferability of the defects is confirmed by observing to what size of the defects can be transferred onto the semiconductor wafer (step S73). Based on results of the confirmation on the transferability, defect specifications for the reticle are determined (step S74).

[0008] Fig. 7B is a flow chart showing procedures of defect inspection of a product reticle on which patterns of an actual product chip are formed.

[0009] First, a product reticle as shown in Fig. 8 is fabricated (step S81).

[0010] Fig. 8 is a plan view of a conventional reticle 81 on which patterns of a product chip are arranged. As shown in Fig. 8, the reticle 81 has a plurality of device patterns 83-1 to 83-4 formed in an exposure area (wafer transfer area) exposed by an exposure apparatus. The device patterns 83-1 to 83-4 are respectively those related to the product chip, and all of which are identical. Outside an exposure area 82 on the reticle 81, alignment patterns 84 used for positional adjustment of the reticle during the light exposure are arranged. Label areas 85 are those for placing the identification numbers or the like of the reticle.

[0011] Referring now back to Fig. 7B, an inspection sensitivity is set based on the defect specifications determined by the preliminary evaluation shown in Fig. 7A (step S82).

[0012] Next, defect inspection of the reticle is carried out (step S83). The defect inspection includes comparative inspection for comparing a plurality of device patterns on the reticle with each other, and data collation inspection for collating the device patterns on the reticle with their design patterns.

[0013] Whether any defect in the pattern was detected or not by

the defect inspection is then judged (step S84). If any defect was detected, the defect is then judged whether it is a substantial defect located on the pattern or not, that is, whether the detected defect should necessarily be corrected or not (step S85). On the other hand, if it was judged in step S84 that no defect was detected, the process comes to the end.

[0014] If the detected defect was judged in step S85 as being a substantial defect, an information on the defect (positional information, etc.) is entered for future correction (step S86).

[0015] If the defect confirmation on the reticle comes to the end, or if there is no need of additional defect confirmation, the process will be terminated, but if not, the process will be returned to step S84 (step S87).

[0016] After completion of processing shown in Fig. 7B, next in a reticle correction process, the defect on the reticle is corrected based on the information on the defect entered in step S86.

[0017] All of the defects in the pattern on the reticle are thus corrected, and the process advances to the next step.

[0018] In some conventional product reticles, a pattern identical to the device pattern of the product chip formed in the

exposure area, or a part of the device pattern is formed on the reticle as a collation pattern used for comparison with the device pattern, separately from such device pattern (see Japanese Patent Application Laid-Open No. Sho 63-163464 and Japanese Patent Application Laid-Open No. Hei 1-244304, for example), for use in the above-described comparative inspection (step S83 in Fig. 7B) for detecting any defect. In the defect inspection of the reticle, presence or absence of the defect is inspected by comparing the device pattern of the product chip, and by confirming the identity therebetween.

SUMMARY OF THE INVENTION

- [0019] A reticle of the present invention has a device pattern formed in an exposure area; and evaluation pattern(s) formed in an area different from the exposure area, for evaluating transferability onto a transfer target of any defect in the exposure area.
- [0020] Because the reticle, which is an inspection target, has the evaluation pattern for evaluating defect transferability, any defect detected in the exposure area can be observed under the same inspection wavelength, or under the same conditions, with the evaluation pattern.
- [0021] A reticle inspection method of the present invention com-

prises a pattern forming step for forming a device pattern and evaluation pattern(s) for evaluating transferability of any defect onto a transfer target on one reticle; a defect inspection step for inspecting presence or absence of any defect in the exposure area on the reticle; and an evaluation step for evaluating transferability onto the transfer target of any defect detected in the defect inspection step. In the pattern forming step, the device pattern is formed in the exposure area, and the evaluation patterns are formed in an area different from the exposure area. In the evaluation step, the transferability of the defect is evaluated based on the detected defect and the evaluation pattern.

[0022] According to the reticle inspection method of the present invention, any defect detected in the exposure area can be compared under the same inspection wavelength with the evaluation pattern formed on the same reticle, and this makes it possible to exactly evaluate the transferability of the detected defect onto the transfer target.

BRIEF DESCRIPTION OF THE DRAWINGS

[0023] Figs. 1A and 1B are flow charts showing an exemplary reticle inspection method according to an embodiment of the present invention;

- [0024] Figs. 2A and 2B are plan views showing an evaluation reticle having programmed defects arranged thereon;
- [0025] Fig. 3 shows transferability of the programmed defects;
- [0026] Figs. 4A to 4C are plan views showing an exemplary product reticle of the embodiment, having device patterns and preliminarily-evaluated programmed defects;
- [0027] Figs. 5A and 5B are drawings for explaining judgment of defect correction in the embodiment;
- [0028] Fig. 6 is a block diagram showing an exemplary configuration of a reticle inspection apparatus of the embodiment;
- [0029] Figs. 7A and 7B are flow charts showing a conventional reticle inspection method; and
- [0030] Fig. 8 is a plan view of a conventional reticle.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

- [0031] The reticle used in photolithography is strictly demanded to be defect-free, or having no defect affective to the pattern transfer. Guaranteed defect size and so forth may vary depending on the design rule, pattern size, geometry, and position of the defect in the individual layers. In general, types and size of the defect to be guaranteed are determined by preliminary experimental data obtained

using an evaluation reticle (preliminary evaluation shown in Fig. 7A, for example), so that the completed product reticle will be observed by an engineer on an inspection apparatus for size and position of the defects, and a judgment will be made on whether the defects are in need of correction or not.

[0032] The conventional reticle inspection, however, is disadvantageous in that the inspection cannot be made under the same condition because of difference in the inspection wavelength and exposure wavelength; that the pattern formed on the reticle and the pattern transferred onto the semiconductor wafer are different in size; and that the operator has to comparatively observe the images; all of these make it rather difficult to judge whether the defects are in need of correction or not, and in some cases, even result in discovery of non-conformities only after the defect inspection.

[0033] Another problem in some conventional reticle inspection resides in that all detected defects having a size larger than a predetermined size are corrected irrespective of their types. This undesirably results in increase in the number of process steps, because even the defects not affective to the pattern transfer are corrected in the cor-

rection process.

[0034] Although some recent approaches have been made on judgment of whether the correction is necessary or not based on simulation, the technique therefor is still on the way to be established.

[0035] An object of the present invention is to enable an exact judgment on whether the defect is in need of correction or not, when the defect is detected in inspection of a product reticle having a device pattern formed thereon.

[0036] The following paragraphs will describe embodiments of the present invention referring to the attached drawings.

[0037] Figs. 1A and 1B are flow charts showing a reticle inspection method according to one embodiment of the present invention, where Fig. 1A shows a preliminary evaluation process for determining defect specifications of the reticle, and Fig. 1B shows a defect inspection process of a product reticle for an actual product chip.

[0038] First, the preliminary evaluation process shown in Fig. 1A will be explained.

[0039] The preliminary evaluation process shown in Fig. 1A is carried out to determine defect specifications of the reticle, typically before defect inspection of a product reticle for manufacturing actual products in which a novel tech-

nology is adopted.

[0040] In step S1, an evaluation reticle as shown in Fig. 2A is fabricated.

[0041] Fig. 2A is a plan view showing an exemplary evaluation reticle.

[0042] The evaluation reticle has a plurality of representative programmed defects preliminarily arranged thereon in the exposure area (wafer transfer area) 21, for the purpose of confirming transferability of any defect of the reticle onto a semiconductor wafer (transfer target). The programmed defects are patterns formed as preliminarily assumed defects on defect-free patterns (see patterns A0, B0, C0, D0 and F0 shown in Fig. 2B), and are arranged in a plural number by types of the defects (defect types) while being varied in size of the defects (defect sizes) on the evaluation reticle. It is to be noted that, in Fig. 2, the pattern portions are indicated by black filling, and on the reticle where glass is used for the substrate, the pattern portions are composed of chromium which can serve as a light interception film.

[0043] The programmed defects typically include chipping Ai, projection Bi, short-circuiting Ci, line breakage Di, isolated residue Ei and isolated pinhole (isolated PH). The suffix "i"

is an integer from 1 to 8 in Fig. 2A. In this figure, the defect size of the individual programmed defects is the smallest when the suffix "i" has a value of 1, and becomes larger as the value of the suffix "i" becomes larger.

[0044] The chipping A_i is a sort of defect characterized by omission of a part of a defect-free pattern A_0 on one edge thereof, and the resultant pattern consequently has a recessed geometry. The projection B_i is a sort of defect characterized by projection of a part of a defect-free pattern B_0 on one edge thereof, and the resultant pattern consequently has a convex geometry. The short-circuiting C_i is a sort of defect characterized by bridging of adjacent defect-free patterns C_0 , and the line breakage D_i is a defect characterized by dividing of one defect-free pattern D_0 into two regions (patterns). The isolated residue E_i is a defect characterized by formation of a dot pattern in an area intrinsically having no pattern, and the isolated pin-hole F_i is a defect characterized by dot-like omission inside a defect-free pattern F_0 .

[0045] Referring now back to Fig. 1A, in step S2, the evaluation reticle having the programmed defects formed thereon is exposed using an actual exposure apparatus to thereby transfer the programmed defects onto a semiconductor

wafer.

[0046] Next in step S3, confirmation is made on which programmed defects on the evaluation reticle were transferred onto the semiconductor wafer, that is, transferability of the programmed defects. By this procedure, to what degree of defect size can be transferred onto the semiconductor wafer is confirmed for every defect type of the programmed defects.

[0047] Next in step S4, defect specifications of the reticle adopted to the product reticle are determined based on confirmed results of the transferability of the programmed defects obtained in step S3.

[0048] Fig. 3 shows exemplary transferability of the programmed defects confirmed in step S3. In this expression, "O" stands for "transferable", and "×" stands for "untransferable".

[0049] More specifically in examples shown in Fig. 3, the programmed defects classified as chipping and having defect sizes of 1 and 2 (A1, A2) show no transferability (that is, the defect portions are not transferred onto the semiconductor wafer), but those having defect sizes of 3 and larger (A3 to A8) show the transferability (that is, the defect portions are transferred onto the semiconductor

wafer). The programmed defects classified as projection and having defect sizes of 3 or smaller (B1, B2, B3) show no transferability, but those having defect sizes of 4 or larger (B4 to B8) show the transferability. Similarly, defect types of short-circuiting, line breakage, isolated residue and isolated pinhole having defect sizes of 5 or larger (C5 to C8), 3 or larger (D3 to D8), 6 or larger (E6 to E8), and 2 or larger (F2 to F8), respectively, show the defect transferability.

[0050] For the case where the transferability of the programmed defects are confirmed as listed in Fig. 3, an inspection sensitivity is set based on defect size 1 for the defect inspection process of the product reticle shown in Fig. 1B, although the transferability differ by the defect types.

[0051] Next paragraphs will describe the defect inspection process of the product reticle shown in Fig. 1B.

[0052] In step S11, a product reticle as shown in Figs. 4A to 4C is fabricated, on which the programmed defects preliminarily evaluated for their defect transferability in the above-described preliminarily evaluation process are arranged as the evaluation patterns together with the device pattern of the actual product chip. That is, a product reticle having the programmed defects preliminarily evaluated for their

defect transferability formed thereon is fabricated.

[0053] Fig. 4A is a plan view showing an exemplary product reticle 41 in the present embodiment.

[0054] As shown in Fig. 4A, the product reticle 41 has a plurality of device patterns 43-1 to 43-4 in the exposure area (wafer transfer area) 42 which is exposed by an exposure apparatus. The device patterns 43-1 to 43-4 have the same geometry pattern respectively related to the product chip.

[0055] The product reticle 41 also has the programmed defects preliminarily evaluated for their defect transferability, in an evaluation pattern area 44 outside the exposure area 42. In other words, the product reticle 41 has, in the evaluation pattern area 44 outside the exposure area 42, the programmed defects which are preliminarily evaluated for their defect transferability, as the evaluation pattern for evaluating transferability of any possible defects in the exposure area 42.

[0056] It is to be understood that Fig. 4A shows only an exemplary case where the evaluation pattern area 44 is disposed on the upper side of the exposure area 42 as viewed in this drawing, and any other area different from the exposure area 42 and at an arbitrary position outside

the exposure area 42 on the product reticle 41 are allowable. The number of disposition of the evaluation pattern area 44 is not limited to one but may be two or more, where the evaluation patterns may be disposed on the upper and lower sides of the exposure area 42, or may be on the upper and left sides, for example.

[0057] The product reticle 41 has, disposed thereon, an alignment pattern 45 for adjusting position of the reticle 41 when an area outside the exposure area 42 is exposed, and a label area 46 in which a numbering or the like for identifying the reticle 41 is given.

[0058] Figs. 4B and 4C are drawing showing exemplary programmed defects disposed in the evaluation pattern area 44.

[0059] Fig. 4B shows an example of the evaluation pattern area 44 having, arranged therein, the programmed defects A2, B3, C4, D2, E5 and F1 having the largest defect sizes in the individual defect types, selected from the programmed defects judged as being untransferable in the above-described preliminary evaluation process (a group of the programmed defects having the smaller "i", out of two groups of the programmed defects divided by boundary line DL with respect to the transferability shown in Fig.

2A),

[0060] On the other hand, Fig. 4C shows an example of the evaluation pattern area 44 having, arranged therein, all programmed defects Ai, Bi, Ci, Di, Ei and Fi (i=1 to 8) already evaluated for their defect transferability in the above-described preliminarily evaluation process.

[0061] The programmed defects arranged herein in the evaluation pattern area 44 are such as those for evaluating the transferability of the defects in the exposure area 42 on the product reticle 41 as described in the above, that is, for evaluating whether the defects can be transferred onto the semiconductor wafer or not when the reticle is exposed by light. It is therefore sufficient for the evaluation pattern area 44 to have, arranged therein as the evaluation pattern, at least the programmed defects A2, B3, C4, D2, E5 and F1 having the largest defect sizes in the individual defect types, out of the programmed defects judged as being untransferable in the above-described preliminary evaluation process, and whether other programmed defects are additionally arranged or not is a matter of arbitrary choice.

[0062] The product reticle 41 herein has the same magnification factor as the evaluation reticle for which the transferability

of the programmed defects is preliminarily evaluated, and the programmed defects arranged in the evaluation pattern area 44 of the product reticle 41 have the same size as the programmed defects arranged on the evaluation reticle. Again in the product reticle 41 shown in Fig. 4A, the substrate is typically composed of glass, and the patterns are composed of chromium which can serve as a light interception film.

[0063] Referring now back to Fig. 1B, in step S12, a defect inspection sensitivity is set based on the defect specifications of the reticle determined in step S4 in the above-described preliminary evaluation process. The defect inspection sensitivity is used for detection of the defects in the device patterns 43-1 to 43-4 in the defect inspection in the next step S13.

[0064] Next in step S13, the defect inspection is carried out in order to inspect presence or absence of any defect in the exposure area 42 on the reticle 41. The defect inspection in step S13 is carried out by a comparison step for comparing the device patterns 43-1 to 43-4 on the product reticle 41 with each other, or by a data collation inspection for collating the device patterns 43-1 to 43-4 respectively with the design data.

[0065] The defect inspection is carried out based on the defect inspection sensitivity set by step S12. This allows any portion of difference in the device patterns 43-1 to 43-4 exceeding the defect inspection sensitivity can be detected as defect, whereas any portion of difference smaller than the defect inspection sensitivity is not judged as defect.

[0066] Next in step S14, whether any defect was detected on the product reticle 41 in the defect inspection in step S13 or not is judged. For the case where the defect was detected, it is further judged that to which defect of real defect, pseudo defect and dust, the detected defect is ascribable. The real defect herein refers to a true defect which occurs in the pattern on the product reticle 41, and pseudo defect is the one assumed as a defect by an inspection apparatus due to inspection error or the like, although the pattern has no defect in reality. The process then advances to step S15 if the detected defect is judged as a real defect, and the defect inspection process is terminated if not. It is to be noted that the real defect will simply be referred to "defect" in the description below.

[0067] Next in step S15, the detected defect and the programmed defects, preliminarily evaluated for their transferability of the defects arranged in the evaluation pattern

area 44 corresponded to the types of detected defects, are compared.

[0068] Next in step S16, correction judgment is made on whether the detected defects should necessary be corrected or not based on comparison results obtained in step S15.

[0069] In short, in steps S15 and S16, transferability of the detected defects onto the semiconductor wafer is evaluated by comparing the detected defects with the programmed defects preliminarily detected for their defect transferability corresponded to the types of such defect.

[0070] The correction judgment in step S16 will be described in the next, referring to Figs. 5A and 5B. Figs. 5A and 5B show an exemplary case where the type of the detected defects is projection.

[0071] Assuming now that a pattern 51 having a defect of projection as shown in Fig. 5A has just detected, wherein comparison of this pattern 51 with the programmed defect B3, having a defect type of projection, having the largest defect size among those having no defect transferability, and being arranged in the evaluation pattern area 44, reveals that the defect portion (projection) is larger than the programmed defect B3 as shown by R1. The defect of the pattern 51 is therefore judged in the

correction judgment as being in need of correction.

[0072] On the other hand, assuming now that a pattern 52 having a defect of projection as shown in Fig. 5B has just detected, wherein similar comparison of this pattern 52 with the programmed defect B3 reveals that the defect portion (projection) is smaller than the programmed defect B3 as shown by R2. The defect of the pattern 52 is therefore judged in the correction judgment as being in no need of correction.

[0073] In other words in the correction judgment in step S16, a detected defect larger than a programmed defect having a largest defect size among those having no transferability in the correspondent defect type is judged as being in need of correction, and if not, judged as being in no need of correction.

[0074] When the detected defect was judged in the correction judgment in step S16 as being in need of correction, information on the detected defect (e.g., position information such as coordinate indicating the defect position, etc.) is entered in step S17. Next in step S18, the defect inspection process is terminated if the confirmation operation of the defect on the product reticle 41 is completed, or no other confirmation operation is found, and if not,

the process returns to step S14.

[0075] After completion of the above-described defect inspection operation, in the correction process for the product reticle, the defect is corrected based on the information on the defect entered in step S17. In this way, all of the transferable defects detected on the product reticle are corrected, and the process advances to the next step.

[0076] Fig. 6 is a block diagram of an exemplary configuration of a reticle inspection apparatus for realizing the reticle inspection method shown in Fig. 1B, and in more detail, for realizing the defect inspection process shown in Fig. 1B. It is to be assumed that the preliminary evaluation process shown in Fig. 1A is already executed by some other apparatus.

[0077] In Fig. 6, reference numeral 61 denotes a inspection sensitivity setting section, in which an inspection sensitivity in the defect inspection for the product reticle is entered and set based on the defect specifications of the reticle determined in the above-described preliminary evaluation process. Reference numeral 62 denotes an irradiation section (reflected light) and 62-2 denotes an irradiation section (transmitted light). The irradiation sections 62, 62-2 irradiate a laser light for scanning the pattern on the product

reticle 63 having the programmed defects preliminarily evaluated for their defect transferability as shown in Figs. 4A to 4C. It is sufficient for the irradiation section to have either one of the irradiation section (reflected light) 62 and irradiation section (transmitted light) 62-2.

[0078] Reference numeral 64 denotes a detection section which detects laser light emitted by the irradiation section (reflected light) 62 and reflected by the product reticle 63, or detects laser light emitted by the irradiation section (transmitted light) 62-2 and allowed to pass through the product reticle 63, and outputs a signal depending on the intensity of light. That is, a signal corresponding to the detection result of the pattern formed on the product reticle 63 is output from the detection section 64.

[0079] Reference numeral 65 denote a device pattern inspection section which inspects presence or absence of any defect (defect inspection) in the device pattern (exposure area) on the product reticle 63 based on the inspection sensitivity set on the inspection sensitivity setting section 61 and the signal output from the detection section 64. More specifically, the device pattern inspection section 65 inspects whether any difference exceeding the previously-set inspection sensitivity is found between the device pat-

tern on the product reticle 63 and other device pattern on the same reticle or design data, based on the signal output from the detection section 64. The device pattern inspection section 65 then outputs inspection results of the defect inspection.

[0080] Reference numeral 66 denotes a transferability evaluation section which evaluates transferability of any defects detected by the device pattern inspection section 65. More specifically, when an inspection result describing that any defect was detected was output from the device pattern inspection section 65, the transferability evaluation section 66 compares, using an image data or a lens, the detected defect with the programmed defects preliminarily evaluated for their defect transferability (programmed defects having no transferability of the defects) and arranged as the evaluation pattern on the product reticle 63, and evaluates the transferability of the detected defects. The transferability evaluation section 66 then judges whether the detected defects are in need of correction or not, based on results of the comparison.

[0081] Reference numeral 67 denotes a defect data memory section which stores position information and so forth with respect to the defects which are judged as being in need

of correction by the transferability evaluation section 66. Reference numeral 68 denotes an output section which outputs information stored in the defect data memory section 67.

[0082] As has been described in the above, the device patterns 43-1 to 43-4 are formed in the exposure area 42 on one product reticle 41, and the programmed defects preliminarily evaluated for their defect transferability are formed as the evaluation pattern for evaluating transferability of the defects in the exposure area, in the evaluation pattern area 44 different from the exposure area 42. When any defect was detected in the exposure area 42 in the inspection of the product reticle 41, transferability of the detected defect is evaluated (judged) by comparing such detected defect with the preliminarily-evaluated programmed defects formed on the same reticle.

[0083] This makes it possible to comparatively observe the detected defects in the exposure area 42 on the product reticle 41 and the preliminarily-evaluated programmed defects under the same inspection wavelength, allows an exact judgment on whether the detected defects are transferable or not, that is, whether the detected defects are adversely affective to transfer of the device pattern or

not, and consequently allows an exact judgment on necessity of the correction of such defects.

[0084] Formation of the preliminarily-evaluated programmed defects on the product reticle as being arranged by types of the defects possibly generated on the product reticle makes it possible to judge transferability of the defect corresponding to types of the defect detected in the exposure area. This makes it possible to judge by types of the defects whether the detected defects are adversely affective to transfer of the device pattern, and allows an exact judgment on the necessity of the correction of the defect. Unlike the conventional technique by which all of the defects having a predetermined size or larger size are corrected irrespective of types of the defects, it is no more necessary in the present invention to correct the defects which are not affective to the device pattern transfer even they have a predetermined size or larger size, and this results in reduced number of process steps.

[0085] Because the programmed defects, having the largest defect size among those judged as showing no defect transferability, are formed as the evaluation pattern so as to be arranged by types of the defects in the evaluation pattern area 44, it is made possible to exactly judge whether the

detected defects has transferability or not only by simply comparing sizes of the detected defect and the programmed defect corresponded to the defect type of such defect, and to extremely simply and exactly judge necessity of the correction of the defect.

[0086] The evaluation patterns for evaluating the defect transferability are formed together with the device pattern on the same reticle, it is made possible to observe any defect detected in the exposure area on the reticle and the evaluation patterns under the same inspection wavelength, and to exactly evaluate the transferability of the detected defect onto the transfer target. This consequently makes it possible to judge whether the detected defect is affective to the transfer of the device pattern or not, and to exactly determine whether the correction of the defect is necessary or not.

[0087] It is to be understood that the above-described embodiment is only one specific example of carrying out the present invention, by which a technical scope of the present invention should not be understood in a limitative manner. In other words, the present invention can be embodied in various modified manner without departing from a technical spirit and main features of the present

invention.